

Overview

The Schematic Viewer allows you to quickly see the logical arrangement of circuit elements from within XJTAG. It is integrated into XJDeveloper and XJRunner and can be launched from any device, net or pin in XJDeveloper or from links embedded in the XJRunner test output. This saves having to continually change applications to view the schematic.

You can use the viewer to quickly understand how a device is being used in a circuit, to help identify faults and debug tests. Simple links take you straight from your XJTAG system and highlight the relevant section of the schematic in the viewer, while the intelligent netlist search allows you to navigate directly to other elements of the circuit.

Included free with XJDeveloper and XJRunner

The Schematic Viewer is integrated as a standard feature of XJDeveloper and XJRunner to help engineers quickly understand the function of the circuit.

Safe and secure

For manufacturers to use the viewer in XJRunner, you have to explicitly export the schematic information from XJDeveloper. Conversely, if your schematics need to be kept confidential, do not export them to XJRunner.

Find the right device, net or pin in seconds

Whether you are developing tests or debugging boards, searching for a device, net or pin on a schematic is a common task. The Schematic Viewer simplifies this process using its smart textual analysis as well as PDF bookmarks to take you to the most relevant section of the schematic.

The viewer is very helpful when setting up your XJTAG test system as you can use simple context menus to view the section of the schematic related to any device. These menus are available

Key Benefits

- Speed up test creation and debugging of prototype and manufactured boards
- Improved search functionality*

Features

- Integrated in XJDeveloper and XJRunner
- Shows best match first when searching for pins/nets/devices
- Intelligent, learning search
- Searches meta data as well as surface text in PDF documents*

*Requires searchable PDF documents

throughout the system and can be used at every stage of the setup, even when reviewing the test coverage that XJTAG reports in its Design For Test analysis.

Once you receive your first prototype hardware and start running tests, the Schematic Viewer saves you time and effort by allowing you to navigate directly to those nets identified as faulty. This can help you understand whether any problems are caused by real hardware faults or are simply configuration issues – such as unfitted components.

If you need to check if a device has been correctly fitted to your board you can use the XJTAG Layout Viewer to pinpoint its physical location.

Generic data import

The viewer can be used with the output from any schematic capture tool that can produce standard PDF documents.

Distributor / Technology Partner

